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PTO/SB/08a (05-03)
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INIEO	DIATIO	N DIOO		Application Number	TOK (01(0175/1	
INFO	RMAIIC	N DISC	LOSURE	Filing Date	Filed Herewith	
STA	STATEMENT BY APPLICANT			First Named Inventor	V. C. JaiPrakash	
				Art Unit	IBA DIKK	
	(use as many sheets as necessary)			Examiner Name	TBA	
Sheet	1	of	2	Attorney Docket Number	03 P 52507 US (90035.211)	

U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No.1	Document Number  Number Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
2001	1	US- 5119153	06-02-1992	Korman, et al.	
101-	2	US- 5225697	07-06-1993	Malhi, et al.	
}	3	US- 6153902	11-28-2000	Furukawa, et al.	
·8~	4	US- 6281539	08-28-2001	Mandelman, et al.	
2	5	US- 6333533	12-25-2001	Furukawa, et al.	
		US			
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Substitu	te for form 1449B/PTO	Complete if Known		
		Application Number	TEX 10/6/15 11	
INFO	DRMATION DISCLOSURE	Filing Date	Filed Herewith	
STATEMENT BY APPLICANT		First Named Inventor	V. C. JaiPrakash	
	TEMENT BY ALL EIGAN	Group Art Unit	PEA DX 11	
_	(use as many sheets as necessary)	Examiner Name	ТВА	
Sheet	2 - of 2	Attorney Docket Number	03 P 52507 US (90035.211)	

		OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS				
Examiner Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²			
LOP	6	Article entitled "A Novel Trench DRAM Cell with a VERTICAL Access Transistor And BuriEd STrap (VERI BEST) for 4 Gb/16Gb by U. Gruening et al., IEDM (IEEE), 1999.				
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Examine Signatur	er t	Date Considered 8/31/04				

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<sup>&</sup>lt;sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.